

RJF0605JPD

Silicon N Channel MOS FET Series Power Switching

R07DS0579EJ0200 Rev.2.00 Feb 22, 2012

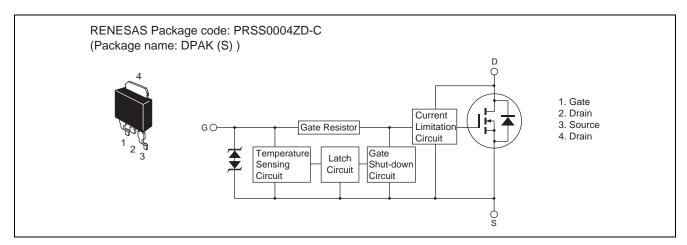
Description

This FET has the over temperature shut-down capability sensing to the junction temperature. This FET has the built-in over temperature shut-down circuit in the gate area. And this circuit operation to shut-down the gate voltage in case of high junction temperature like applying over power consumption, over current etc..

Features

- Logic level operation (5 V Gate drive).
- Built-in the over temperature shut-down circuit.
- High endurance capability against to the short circuit.
- Latch type shut down operation (need 0 voltage recovery).
- Built-in the current limitation circuit.
- Power supply voltage applies 12 V and 24 V.
- AEC-Q101 Compliant

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	60	V
Gate to source voltage	V_{GSS}	16	V
Gate to source voltage	V_{GSS}	-2.5	V
Drain current	I _D Note3	20	Α
Body-drain diode reverse drain current	I _{DR}	20	А
Avalanche current	I _{AP} Note 2	(6.7)	А
Avalanche energy	E _{AR} Note 2	(192)	mJ
Channel dissipation	Pch Note 1	40	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. Value at Tc = 25°C

- 2. Tch = 25°C, Rg \geq 50 Ω
- 3. It provides by the current limitation lower bound value.

Typical Operation Characteristics

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Input voltage	V_{IH}	3.5	_	_	V	
	V_{IL}	_	_	1.2	V	
Input current	I _{IH1}	1	_	100	μΑ	$Vi = 8 V, V_{DS} = 0$
(Gate non shut down)	I _{IH2}	1	_	50	μΑ	$Vi = 3.5 V, V_{DS} = 0$
	I _{IL}	1	_	1	μΑ	Vi = 1.2 V, V _{DS} = 0
Input current	I _{IH(sd)1}	1	0.8	_	mA	$Vi = 8 V, V_{DS} = 0$
(Gate shut down)	I _{IH(sd)2}	1	0.35	_	mA	$Vi = 3.5 V, V_{DS} = 0$
Shut down temperature	Tsd	1	175	_	°C	Channel temperature
Gate operation voltage	Vop	3.5	_	12	V	
Drain current	I _{D limt}	(20)	_	_	Α	$V_{GS} = 5 \text{ V}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$
(Current limitation value)						

Note; 4. Pulse test

Electrical Characteristics

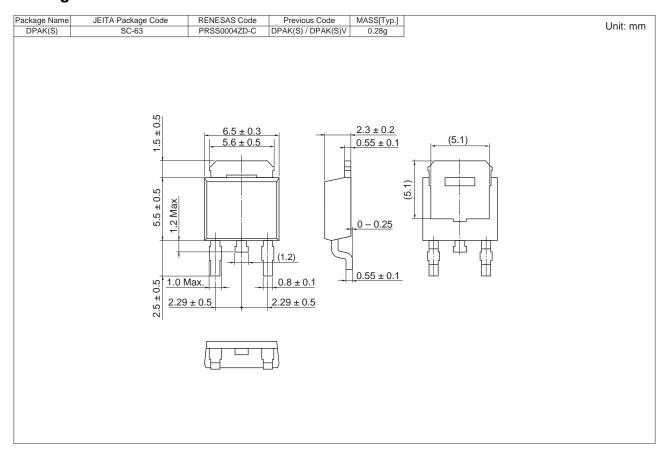
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain current	I _{D1}	_	_	(28)	Α	$V_{GS} = 3.5 \text{ V}, V_{DS} = 10 \text{ V}^{\text{Note 5}}$
	I _{D2}	_	_	10	mA	$V_{GS} = 1.2 \text{ V}, V_{DS} = 10 \text{ V}$
	I _{D3}	(20)	_	_	Α	$V_{GS} = 5 \text{ V}, V_{DS} = 10 \text{ V}^{\text{Note 5}}$
Drain to source breakdown voltage	V _{(BR)DSS}	60	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown	$V_{(BR)GSS}$	16	_	_	V	$I_G = 800 \ \mu A, \ V_{DS} = 0$
voltage	$V_{(BR)GSS}$	-2.5	_	_	V	$I_G = -100 \mu A, V_{DS} = 0$
Gate to source leak current	I _{GSS1}	_	_	100	μΑ	$V_{GS} = 8 \text{ V}, V_{DS} = 0$
	I _{GSS2}	_	_	50	μΑ	$V_{GS} = 3.5 \text{ V}, V_{DS} = 0$
	I _{GSS3}	_	_	1	μΑ	$V_{GS} = 1.2 \text{ V}, V_{DS} = 0$
	I _{GSS4}	_	_	-100	μΑ	$V_{GS} = -2.4 \text{ V}, V_{DS} = 0$
Input current (shut down)	I _{GS(OP)1}		0.8	_	mA	$V_{GS} = 8 \text{ V}, V_{DS} = 0$
	I _{GS(OP)2}	_	0.35	_	mA	$V_{GS} = 3.5 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS1}	_	_	10	μΑ	$V_{DS} = 32 \text{ V}, V_{GS} = 0$
	I _{DSS2}		_	(10)	μΑ	$V_{DS} = 60 \text{ V}, V_{GS} = 0, Ta = 110^{\circ}\text{C}$
Gate to source cutoff voltage	$V_{GS(off)}$	(1.2)	_	(2.4)	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Forward transfer admittance	y _{fs}	(12)	(20)	_	S	$I_D = 10 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 5}}$
Static drain to source on state	R _{DS(on)}		(37)	50	mΩ	$I_D = 10 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note 5}}$
resistance	R _{DS(on)}	_	(27)	(38)	mΩ	$I_D = 10 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 5}}$
Output capacitance	Coss	_	(450)	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1 \text{MHz}$
Turn-on delay time	t _{d(on)}		(8)	_	μS	$V_{GS} = 5 \text{ V}, I_{D} = 10 \text{ A}, R_{L} = 3 \Omega$
Rise time	t _r	_	(36)	_	μS	
Turn-off delay time	t _{d(off)}	_	(2.8)	_	μS	
Fall time	t _f		(6.8)	_	μS	
Body-drain diode forward	V_{DF}		(0.9)		V	I _F = 20 A, V _{GS} = 0
voltage						
Body-drain diode reverse	t _{rr}	_	(100)	_	ns	$I_F = 20 \text{ A}, V_{GS} = 0$
recovery time						di _F /dt = 50 A/μs
Over load shut down	t _{os1}	_	(0.45)	_	ms	$V_{GS} = 5 \text{ V}, V_{DD} = 16 \text{ V}$
operation time Note 6	t _{os2}	_	(0.35)	_	ms	$V_{GS} = 5 \text{ V}, V_{DD} = 24 \text{ V}$

Notes: 5. Pulse test

6. Including the junction temperature rise of the over loaded condition.

Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJF0605JPD-00#J3	3000 pcs	Taping

Note: The symbol of a "#" are occasionally presented as a "-".

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